

Date Filed: 5/18/2007

PTO/SB/08a (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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	First Named Inventor	Jim NEWTON		
	Art Unit	2825		
	Examiner Name	Kik, Phallaka		
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/PK/	1	Cadence "Cell Design Tutorial" Title Page, Table of Contents, Chapters 5 and 6, June 2000, Cadence Design Systems, Inc., San Jose, CA, pp. 1-6, 131-195.	<input type="checkbox"/>
/PK/	2	Cadence "Virtuoso Parameterized Cell Reference" 1999, 35 pgs., Cadence Design Systems, Inc., San Jose, CA.	<input type="checkbox"/>
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/PK/	4	JINGNAN, X. et al. "A SKILL-based Library for Retargetable Embedded Analog Cores" Proceedings of the Conference on Design, Automation and Test in Europe, Munich, Germany, 2001, pp. 768-769.	<input type="checkbox"/>
/PK/	5	ZAMAN, M.H. et al. "An Environment for Design and Modeling of Electro-Mechanical Micro-Systems" Journal of Modeling and Simulation of Microsystems, 1999, Vol. 1, No. 1, pp. 65-76, located at http://www.conventor.com/media/papers/jmsm_1999.pdf	<input type="checkbox"/>

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